

L Number	Hits	Search Text	DB	Time stamp
1	7862	first adj delay	USPAT	2003/08/25 16:45
2	12209	second adj delay	USPAT	2003/08/25 16:45
3	6287	(first adj delay) and (second adj delay)	USPAT	2003/08/25 16:45
4	1865	((first adj delay) and (second adj delay)) and (flip adj flop)	USPAT	2003/08/25 16:45
5	898	((((first adj delay) and (second adj delay)) and (flip adj flop)) and (selector or selecting))	USPAT	2003/08/25 16:46
6	388	(((((first adj delay) and (second adj delay)) and (flip adj flop)) and (selector or selecting)) and (plurality with delay))	USPAT	2003/08/25 16:46
7	235	(((((first adj delay) and (second adj delay)) and (flip adj flop)) and (selector or selecting)) and (plurality with delay)) and (detector or detecting)	USPAT	2003/08/25 16:47
8	5	("4618787" "5440592" "5818890" "6025744" "6025745").PN.	USPAT	2003/08/25 16:57
9	5	("5022056" "5166959" "5245231" "5349612" "5400370").PN.	USPAT	2003/08/25 16:57
10	5	("4618787" "5374860" "5530387" "5708382" "5818890").PN.	USPAT	2003/08/25 17:00

Titles of Most Frequently Occurring Classifications of Patents Returned
From A Search of 10078340 on December 03, 2002

- 23 365/233 (13 OR, 10 XR)
Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
365/230.01 ADDRESSING
365/233 .Sync/clocking
- 14 365/194 (3 OR, 11 XR)
Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
365/189.01 READ/WRITE CIRCUIT
365/191 .Signals
365/194 ..Delay
- 8 365/189.05 (0 OR, 8 XR)
Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
365/189.01 READ/WRITE CIRCUIT
365/189.05 .Having particular data buffer or latch
- 8 365/203 (2 OR, 6 XR)
Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
365/189.01 READ/WRITE CIRCUIT
365/203 .Precharge
- 6 365/230.03 (1 OR, 5 XR)
Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
365/230.01 ADDRESSING
365/230.03 .Plural blocks or banks
- 6 365/230.08 (0 OR, 6 XR)
Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
365/230.01 ADDRESSING
365/230.08 .Including particular address buffer or latch
circuit arrangement
- 6 365/233.5 (1 OR, 5 XR)
Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
365/230.01 ADDRESSING
365/233 .Sync/clocking
365/233.5 ..Transition detection
- 4 365/230.06 (1 OR, 3 XR)
Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
365/230.01 ADDRESSING
365/230.06 .Particular decoder or driver circuit
- 4 711/167 (1 OR, 3 XR)
Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
SYSTEMS: MEMORY
711/100 STORAGE ACCESSING AND CONTROL
711/167 .Access timing
- 3 365/189.01 (1 OR, 2 XR)
Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
365/189.01 READ/WRITE CIRCUIT
- 3 365/189.06 (0 OR, 3 XR)
Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
365/189.01 READ/WRITE CIRCUIT
365/189.06 .Including signal clamping

- 3 365/189.08 (0 OR, 3 XR)
 - Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
 - 365/189.01 READ/WRITE CIRCUIT
 - 365/189.08 ..Including specified plural element logic arrangement
- 3 365/201 (3 OR, 0 XR)
 - Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
 - 365/189.01 READ/WRITE CIRCUIT
 - 365/201 ..Testing
- 3 365/205 (1 OR, 2 XR)
 - Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
 - 365/189.01 READ/WRITE CIRCUIT
 - 365/205 ..Flip-flop used for sensing
- 3 365/230.01 (1 OR, 2 XR)
 - Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
 - 365/230.01 ADDRESSING
- 3 711/169 (3 OR, 0 XR)
 - Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: MEMORY
 - 711/100 STORAGE ACCESSING AND CONTROL
 - 711/167 ..Access timing
 - 711/169 ..Memory access pipelining
- 2 327/158 (0 OR, 2 XR)
 - Class 327 : MISCELLANEOUS ACTIVE ELECTRICAL NONLINEAR DEVICES, CIRCUITS, AND SYSTEMS
 - 327/100 SIGNAL CONVERTING, SHAPING, OR GENERATING
 - 327/141 ..Synchronizing
 - 327/155 ..With feedback
 - 327/156 ...Phase lock loop
 - 327/158With variable delay means
- 2 365/189.07 (0 OR, 2 XR)
 - Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
 - 365/189.01 READ/WRITE CIRCUIT
 - 365/189.07 ..Including signal comparison
- 2 365/189.11 (0 OR, 2 XR)
 - Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
 - 365/189.01 READ/WRITE CIRCUIT
 - 365/189.11 ..Including level shift or pull-up circuit
- 2 365/193 (1 OR, 1 XR)
 - Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
 - 365/189.01 READ/WRITE CIRCUIT
 - 365/191 ..Signals
 - 365/193 ..Strobe
- 2 711/105 (0 OR, 2 XR)
 - Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: MEMORY
 - 711/100 STORAGE ACCESSING AND CONTROL
 - 711/101 ..Specific memory composition
 - 711/104 ..Solid-state random access memory (RAM)
 - 711/105 ...Dynamic random access memory

10078340_CLSTITLES

2 713/401 (1 OR, 1 XR)

Class 713 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
SYSTEMS: SUPPORT

713/400 SYNCHRONIZATION OF CLOCK OR TIMING SIGNALS,
DATA, OR PULSES

713/401 .Using delay